

=====
= List all test points by net -- job#.pcb -- Wed Dec 19 12:02:40 2001
=====

+3.3V #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

TP6.1 8525 2400 BOTTOM

+3.3VA #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8175 2825 BOTTOM

+5CAMERA #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J20.7 7475 9875 BOTTOM

+5CAMERAON- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7150 9200 BOTTOM

+5SENSE #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8750 4100 BOTTOM

+5SENSORS #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J10.1 375 4825 BOTTOM

+5SENSORS- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 1475 3450 BOTTOM

+5V #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

TP3.1 8400 6875 BOTTOM

+12CAMERA #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

C62.1 7350 8450 BOTTOM

+12SENSE #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8550 3850 BOTTOM

+12V #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

TP4.1 8425 8975 BOTTOM

+24SENSE #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8700 3150 BOTTOM

+24V #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

F1.2 4000 9250 BOTTOM

+24VM #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

F7.2 5375 6225 BOTTOM

/DS #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6625 2650 BOTTOM

/J_RESET #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8000 6250 BOTTOM

/J_TRST #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8075 4750 BOTTOM

/PORT_TRST #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J1.11 8825 5525 BOTTOM

/PS #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7075 4675 BOTTOM

/RD #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7200 6000 BOTTOM

/RESET #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8550 5075 BOTTOM

/TRST #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7750 5175 BOTTOM

/WR #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6325 5625 BOTTOM

A0 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7075 4275 BOTTOM

A1 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7125 3500 BOTTOM

A2 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7075 4575 BOTTOM

A3 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6650 2525 BOTTOM

A4 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6550 3300 BOTTOM

A5 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6900 2475 BOTTOM

A6 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6250 3425 BOTTOM

A7 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7150 3350 BOTTOM

A8 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6425 3025 BOTTOM

A9 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6225 3150 BOTTOM

A10 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6425 3275 BOTTOM

A11 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6475 3125 BOTTOM

A12 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7075 3250 BOTTOM

A13 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6575 3000 BOTTOM

A14 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6700 3075 BOTTOM

A15 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8325 4550 BOTTOM

A15- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8225 4600 BOTTOM

A15-RD #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8300 4825 BOTTOM

AGND #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8100 2700 BOTTOM

BARCODELED #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J3.4 2125 1575 BOTTOM

BARCODEON #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 975 1550 BOTTOM

BUFFEREN- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 8100 4525 BOTTOM

CAMERAON #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6750 675 BOTTOM

CARRIERSLOTLED #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J3.6 2325 1575 BOTTOM

CFILTERHOME- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 2600 6175 BOTTOM

CHASSIS #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

C54.2 3800 9800 BOTTOM

CLOADER- #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J19.3 7293 10980 BOTTOM

CPCDATA- #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J19.6 7620 10980 BOTTOM

CSLOTSSENSOR- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 2675 1975 BOTTOM

CURRENT0 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3925 925 BOTTOM

CURRENT1 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3925 6550 BOTTOM

CURRENT2 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3925 3875 BOTTOM

CURRENT3 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3175 7825 BOTTOM

CURRENT4 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3925 5200 BOTTOM

CURRENT5 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 3925 2250 BOTTOM

D0 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6500 6075 BOTTOM

D1 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 5950 2725 BOTTOM

D2 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6650 3500 BOTTOM

D3 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6800 1500 BOTTOM

D4 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6225 700 BOTTOM

D5 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6125 3300 BOTTOM

D6 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6375 3475 BOTTOM

D7 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 6250 6100 BOTTOM

D8 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

TESTPOINTVIA 7250 2925 BOTTOM

D9 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1

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TESTPOINTVIA 7725 450 BOTTOM

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D10 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 6050 3025 BOTTOM

-----
D11 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 7100 1550 BOTTOM

-----
D12 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 5850 2475 BOTTOM

-----
D13 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 6275 2725 BOTTOM

-----
D14 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 7425 1375 BOTTOM

-----
D15 #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 7475 1125 BOTTOM

-----
DEBUG_EVENT #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 8025 3725 BOTTOM

-----
DIP0 #TP= 1 #TP_PINS= 1 #TP_VIAS= 0
-----
S2.1 5575 700 BOTTOM

-----
DIP1 #TP= 1 #TP_PINS= 1 #TP_VIAS= 0
-----
S2.2 5575 600 BOTTOM

-----
DIP2 #TP= 1 #TP_PINS= 1 #TP_VIAS= 0
-----
S2.3 5575 500 BOTTOM

-----
DIP3 #TP= 1 #TP_PINS= 1 #TP_VIAS= 0
-----
S2.4 5575 400 BOTTOM

-----
DISABLE #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 7875 6350 BOTTOM

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DOORSW- #TP= 1 #TP_PINS= 0 #TP_VIAS= 1
-----
TESTPOINTVIA 2100 2025 BOTTOM

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ENABLEJTAG- #TP= 1 #TP_PINS= 1 #TP_VIAS= 0

J1.10 8925 5625 BOTTOM

=====
= List all nets without test points -- job#.pcb -- Wed Dec 19 12:02:40 2001
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=====
= List all nets and whether they have testpoints -- job#.pcb -- Wed Dec 19 12:02:40 2001
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Signal	TP in net?	#TP
+3.3V	YES	1
+3.3VA	YES	1
+5CAMERA	YES	1
+5CAMERAON-	YES	1
+5SENSE	YES	1
+5SENSORS	YES	1
+5SENSORS-	YES	1
+5V	YES	1
+12CAMERA	YES	1
+12SENSE	YES	1
+12V	YES	1
+24SENSE	YES	1
+24V	YES	1
+24VM	YES	1
/DS	YES	1
/J_RESET	YES	1
/J_TRST	YES	1
/PORT_TRST	YES	1
/PS	YES	1
/RD	YES	1
/RESET	YES	1
/TRST	YES	1
/WR	YES	1
A0	YES	1
A1	YES	1
A2	YES	1
A3	YES	1
A4	YES	1
A5	YES	1
A6	YES	1
A7	YES	1
A8	YES	1
A9	YES	1
A10	YES	1
A11	YES	1
A12	YES	1
A13	YES	1
A14	YES	1
A15	YES	1
A15-	YES	1
A15-RD	YES	1
AGND	YES	1
BARCODELED	YES	1
BARCODEON	YES	1
BUFFEREN-	YES	1
CAMERAON	YES	1
CARRIERSLOTLED	YES	1
CFILTERHOME-	YES	1
CHASSIS	YES	1

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CLOADER-   YES    1
CPCDATA-   YES    1
CSLOTSSENSOR- YES  1
CURRENT0   YES    1
CURRENT1   YES    1
CURRENT2   YES    1
CURRENT3   YES    1
CURRENT4   YES    1
CURRENT5   YES    1
D0         YES    1
D1         YES    1
D2         YES    1
D3         YES    1
D4         YES    1
D5         YES    1
D6         YES    1
D7         YES    1
D8         YES    1
D9         YES    1
D10        YES    1
D11        YES    1
D12        YES    1
D13        YES    1
D14        YES    1
D15        YES    1
DEBUG_EVENT YES    1
DIP0       YES    1
DIP1       YES    1
DIP2       YES    1
DIP3       YES    1
DISABLE    YES    1
DOORSW-    YES    1
ENABLEJTAG- YES    1

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=====
= All TP on board, by net -- job#.pcb -- Wed Dec 19 12:02:41 2001
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Signal name  TP Name    X      Y      Side
-----
+3.3V       TP6.1      8525   2400   BOTTOM
+3.3VA      TESTPOINTVIA 8175   2825   BOTTOM
+5CAMERA    J20.7      7475   9875   BOTTOM
+5CAMERAON- TESTPOINTVIA 7150   9200   BOTTOM
+5SENSE     TESTPOINTVIA 8750   4100   BOTTOM
+5SENSORS   J10.1      375    4825   BOTTOM
+5SENSORS-  TESTPOINTVIA 1475   3450   BOTTOM
+5V         TP3.1      8400   6875   BOTTOM
+12CAMERA   C62.1      7350   8450   BOTTOM
+12SENSE    TESTPOINTVIA 8550   3850   BOTTOM
+12V        TP4.1      8425   8975   BOTTOM
+24SENSE    TESTPOINTVIA 8700   3150   BOTTOM
+24V        F1.2       4000   9250   BOTTOM
+24VM       F7.2       5375   6225   BOTTOM
/DS         TESTPOINTVIA 6625   2650   BOTTOM
/J_RESET    TESTPOINTVIA 8000   6250   BOTTOM
/J_TRST     TESTPOINTVIA 8075   4750   BOTTOM
/PORT_TRST  J1.11     8825   5525   BOTTOM
/PS         TESTPOINTVIA 7075   4675   BOTTOM
/RD         TESTPOINTVIA 7200   6000   BOTTOM
/RESET      TESTPOINTVIA 8550   5075   BOTTOM
/TRST       TESTPOINTVIA 7750   5175   BOTTOM
/WR         TESTPOINTVIA 6325   5625   BOTTOM
A0          TESTPOINTVIA 7075   4275   BOTTOM
A1          TESTPOINTVIA 7125   3500   BOTTOM
A2          TESTPOINTVIA 7075   4575   BOTTOM

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A3	TESTPOINTVIA 6650	2525	BOTTOM	
A4	TESTPOINTVIA 6550	3300	BOTTOM	
A5	TESTPOINTVIA 6900	2475	BOTTOM	
A6	TESTPOINTVIA 6250	3425	BOTTOM	
A7	TESTPOINTVIA 7150	3350	BOTTOM	
A8	TESTPOINTVIA 6425	3025	BOTTOM	
A9	TESTPOINTVIA 6225	3150	BOTTOM	
A10	TESTPOINTVIA 6425	3275	BOTTOM	
A11	TESTPOINTVIA 6475	3125	BOTTOM	
A12	TESTPOINTVIA 7075	3250	BOTTOM	
A13	TESTPOINTVIA 6575	3000	BOTTOM	
A14	TESTPOINTVIA 6700	3075	BOTTOM	
A15	TESTPOINTVIA 8325	4550	BOTTOM	
A15-	TESTPOINTVIA 8225	4600	BOTTOM	
A15-RD	TESTPOINTVIA 8300	4825	BOTTOM	
AGND	TESTPOINTVIA 8100	2700	BOTTOM	
BARCODELED	J3.4	2125	1575	BOTTOM
BARCODEON	TESTPOINTVIA 975	1550	BOTTOM	
BUFFEREN-	TESTPOINTVIA 8100	4525	BOTTOM	
CAMERAON	TESTPOINTVIA 6750	675	BOTTOM	
CARRIERSLOTLED	J3.6	2325	1575	BOTTOM
CFILTERHOME-	TESTPOINTVIA 2600	6175	BOTTOM	
CHASSIS	C54.2	3800	9800	BOTTOM
CLOADER-	J19.3	7293	10980	BOTTOM
CPCDATA-	J19.6	7620	10980	BOTTOM
CSLOTSSENSOR-	TESTPOINTVIA 2675	1975	BOTTOM	
CURRENT0	TESTPOINTVIA 3925	925	BOTTOM	
CURRENT1	TESTPOINTVIA 3925	6550	BOTTOM	
CURRENT2	TESTPOINTVIA 3925	3875	BOTTOM	
CURRENT3	TESTPOINTVIA 3175	7825	BOTTOM	
CURRENT4	TESTPOINTVIA 3925	5200	BOTTOM	
CURRENT5	TESTPOINTVIA 3925	2250	BOTTOM	
D0	TESTPOINTVIA 6500	6075	BOTTOM	
D1	TESTPOINTVIA 5950	2725	BOTTOM	
D2	TESTPOINTVIA 6650	3500	BOTTOM	
D3	TESTPOINTVIA 6800	1500	BOTTOM	
D4	TESTPOINTVIA 6225	700	BOTTOM	
D5	TESTPOINTVIA 6125	3300	BOTTOM	
D6	TESTPOINTVIA 6375	3475	BOTTOM	
D7	TESTPOINTVIA 6250	6100	BOTTOM	
D8	TESTPOINTVIA 7250	2925	BOTTOM	
D9	TESTPOINTVIA 7725	450	BOTTOM	
D10	TESTPOINTVIA 6050	3025	BOTTOM	
D11	TESTPOINTVIA 7100	1550	BOTTOM	
D12	TESTPOINTVIA 5850	2475	BOTTOM	
D13	TESTPOINTVIA 6275	2725	BOTTOM	
D14	TESTPOINTVIA 7425	1375	BOTTOM	
D15	TESTPOINTVIA 7475	1125	BOTTOM	
DEBUG_EVENT	TESTPOINTVIA 8025	3725	BOTTOM	
DIP0	S2.1	5575	700	BOTTOM
DIP1	S2.2	5575	600	BOTTOM
DIP2	S2.3	5575	500	BOTTOM
DIP3	S2.4	5575	400	BOTTOM
DISABLE	TESTPOINTVIA 7875	6350	BOTTOM	
DOORSW-	TESTPOINTVIA 2100	2025	BOTTOM	
ENABLEJTAG-	J1.10	8925	5625	BOTTOM